Subscribe (Full Service) Register (Limited Service, Free) Login

Search: The ACM Digital Library The Guide

+skew +compensation +author:WU +author:Leon

2.11.21

Nothing Found

Your search for +skew +compensation +author:WU +author:Leon did not return any results.

You may want to try an Advanced Search for additional options.

Please review the Quick Tips below or for more information see the Search Tips.

Quick Tips

• Enter your search terms in <u>lower case</u> with a space between the terms.

sales offices

You can also enter a full question or concept in plain language.

Where are the sales offices?

 Capitalize proper nouns to search for specific people, places, or products.

John Colter, Netscape Navigator

Enclose a phrase in double quotes to search for that exact phrase.

"museum of natural history" "museum of modern art"

 Narrow your searches by using a + if a search term <u>must appear</u> on a page.

museum +art

• Exclude pages by using a - if a search term <u>must not appear</u> on a page.

museum -Paris

Combine these techniques to create a specific search query. The better your description of the information you want, the more relevant your results will be.

museum +"natural history" dinosaur -Chicago

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2006 ACM, Inc. Terms of Usage Privacy Policy Code of Ethics Contact Us

Useful downloads: Adobe Acrobat QuickTime Windows Media Player



Subscribe (Full Service) Register (Limited Service, Free) Login

Search:

The ACM Digital Library
The Guide

+abstract:skew +abstract:compensation abstract:shared abst

THE ACM DIGITAL LIBRARY

Feedback Report a problem Satisfaction survey

Terms used skew compensation shared bus

Found 6 of 173,942

Relevance scale ...

Sort results by Display results	relevance • expanded form •	Save results to a Binder Search Tips Open results in a new window	Try an <u>Advanced Search</u> Try this search in <u>The ACM Guide</u>
--	-----------------------------	---	---

Results 1 - 6 of 6

1	Demonstrations: BigBatch: a toolbox for monochromatic documents	
	Rafael Dueire Lins, Bruno Tenório Ávila November 2005 Proceedings of the 2005 ACM symposium on Document engineering	
W	November 2005 Proceedings of the 2005 ACM symposium on Document engineering	

DocEng '05
Publisher: ACM Press

Full text available: pdf(419.71 KB) Additional Information: full citation, abstract, references, index terms

BigBatch is a tool designed to automatically process thousands of monochromatic images of documents generated by production line scanners. It removes noisy borders, checks and corrects orientation, calculates and compensates the skew angle, crops the image standardizing document sizes, and finally compresses it according to user defined file format. BigBatch encompasses the best and recently developed algorithms for such kind of document images. BigBatch may work either in standalone or operator ...

Keywords: border removal, document processing, image processing, monochromatic images, orientation, skew detection

2		_
	Modeling high-dimensional index structures using sampling	
	Christian A. Lang, Ambuj K. Singh May 2001 ACM SIGMOD Record, Proceedings of the 2001 ACM SIGMOD international	
V		
	conference on Management of data SIGMOD '01, Volume 30 Issue 2	

Publisher: ACM Press

Full text available: pdf(298.62 KB)

Additional Information: full citation, abstract, references, citings, index terms

A large number of index structures for high-dimensional data have been proposed previously. In order to tune and compare such index structures, it is vital to have efficient cost prediction techniques for these structures. Previous techniques either assume uniformity of the data or are not applicable to high-dimensional data. We propose the use of sampling to predict the number of accessed index pages during a query execution. Sampling is independent of the dimensionality and preserves cluste ...

3	Evolutionary features of genomes as disclosed by comparative analysis of complete	
	genome sequences (abstract only)	

Takashi Gojobori, T. Daniel Andrews, Takeshi Itoh

April 2000 Proceedings of the fourth annual international conference on Computational molecular biology

Publisher: ACM Press

Full text available: pdf(53.45 KB) Additional Information: full citation, abstract

Our comparisons of complete genome sequences revealed that the genome structures have been extensively shuffled among eubacteria, particularly when the orders of

orthologous genes were examined. Moreover, archaebacterial and eukaryotic genome structures were found to be unstable, too, as were the cases of eubacteria. We then turned our attention to operon structures, which were expected to be well conserved during evolution because of their regulatory importance. Surprisingly enough, however, ...

4	MPEG-2 coded- and uncoded-stream synchronization control for real-time multimedia	
	transmission and presentation over B-ISDN	
	L. Li, N. Georganas	
	October 1994 Proceedings of the second ACM international conference on Multimedia	
	Publisher: ACM Press	
	Full text available: pdf(893.22 KB) Additional Information: full citation, abstract, references, citings, index terms	
	A real-time multimedia communication system over broadband networks is introduced in the paper. This system consists of distributed database servers which store and retrieve data objects of different types of media and in different coding formats. The multimedia document is transmitted over the network as streams through different connections and presented to the user simultaneously. A set of stream synchronization control schemes is designed to control the multiple data streams (either in	
5 (Localization and timesynch: The flooding time synchronization protocol Miklós Maróti, Branislav Kusy, Gyula Simon, Ákos Lédeczi November 2004 Proceedings of the 2nd international conference on Embedded	
	networked sensor systems Publisher: ACM Press	
	Full text available: pdf(178.40 KB) Additional Information: full citation, abstract, references, citings, index terms	
	Wireless sensor network applications, similarly to other distributed systems, often require a scalable time synchronization service enabling data consistency and coordination. This paper describes the Flooding Time Synchronization Protocol (FTSP), especially tailored for applications requiring stringent precision on resource limited wireless platforms. The proposed time synchronization protocol uses low communication bandwidth and it is robust against node and link failures. The FTSP achieves	
	Keywords: clock drift, clock synchronization, multi-hop, sensor networks, time synchronization	
6	(Special session) presentation + poster disscussion: university design contest: A reliable low-power fast skew-compensation circuit Yi-Ming Wang, Jinn-Shyan Wang January 2004 Proceedings of the 2004 conference on Asia South Pacific design automation: electronic design and solution fair ASP-DAC '04, Proceedings of the 2004 conference on Asia South Pacific design	
	automation: electronic design and solution fair ASP-DAC '04 Publisher: IEEE Press, IEEE Press	
	Full text available: pdf(2.48 MB) Additional Information: full citation, abstract, references Publisher Site	
	A reliable low-power fast skew-compensation circuit is proposed. Operating on the clock with a 50% duty cycle, the new design is more reliable compared to conventional SMD-based circuits [1]-[3], which can operate only on the pulsed clock. This new circuit also gets phase locking within two clock cycles. The test circuit works successfully between 600-MHz \sim 800-MHz with a power consumption of 25- μ W/MHz \sim 36- μ W/MHz. When measured at 616.9-MHz and 791.4-MHz, the static phase is 76.8-ps and 1	

Results 1 - 6 of 6

The ACM Portal is published by the Association for Computing Machinery. Copyright © 2006 ACM, Inc. Terms of Usage Privacy Policy Code of Ethics Contact Us

Useful downloads: Adobe Acrobat QuickTime Windows Media Player Real Player



Welcome United States Patent and Trademark Office

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

e-mail and printer triendly

Results for	"(skew	compensation <in>metadata)</in>

Your search matched 41 of 1335860 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options <u>View Session History</u>		Mos	iiy:	Search	Search >
		(ske	w co	mpensation <in>metadata)</in>	
New Search			Che	ck to search only within this results set	
		Dis	play	Format:	
» Key					
IEEE JNI.	IEEE Journal or Magazine	₹(<u>vi</u>	•w s	elected items Select All Deselect All	1-25 <u>26-41</u>
IEE JNL	IEE Journal or Magazine	بسم	1.	Low-latency skew-compensation circuits for parallel optical Ir	nterconnections
IEEE CNF	IEEE Conference Proceeding		••	Sakamoto, T.; Tanaka, N.; Ando, Y.; Electronic Components and Technology Conference, 1999, 1999 I	
IEE CNF	IEE Conference Proceeding			1-4 June 1999 Page(s):938 - 944 Digital Object Identifier 10.1109/ECTC.1999.776298	
IEEE STD	IEEE Standard			AbstractPlus Full Text: PDF(584 KB) IEEE CNF Rights and Permissions	
			2.	A low-power half-delay-line fast skew-compensation circuit Yi-Ming Wang; Jinn-Shyan Wang; Solid-State Circuits, IEEE Journal of Volume 39, Issue 6, June 2004 Page(s):906 - 918 Digital Object Identifier 10.1109/JSSC.2004.827800	
				<u>AbstractPlus References Full Text: PDF(1120 KB) IEEE JNL Rights and Permissions</u>	
			3.	A dynamic clock skew compensation circuit technique for low Yamashita, T.; Fujimoto, T.; Ishibashi, K.; Integrated Circuit Design and Technology, 2005, ICICDT 2005, 20 on 9-11 May 2005 Page(s):7 - 10 Digital Object Identifier 10.1109/ICICDT.2005.1502576	
				AbstractPlus Full Text: PDF(211 KB) IEEE CNF Rights and Permissions	
	Ç		4.	Demonstration of timing skew compensation for bit-parallel Webicosecond precision Shen, S.; Weiner, A.M.; Lasers and Electro-Optics, 1999, CLEO '99, Summaries of Papers on 23-28 May 1999 Page(s):389 - 390 Digital Object Identifier 10.1109/CLEO.1999.834348	
				AbstractPlus Full Text: PDF(224 KB)	
		m	5.	Long-distance parallel data link using WDM transmission with Gibong Jeong; Goodman, J.W.; <u>Lightwave Technology, Journal of</u> Volume 14, Issue 5, May 1996 Page(s):655 - 660 Digital Object Identifier 10.1109/50.495142	n bit-skew compensation
				AbstractPlus References Full Text: PDF(700 KB) IEEE JNL Rights and Permissions	

	6. A 2.4 Gb/s/pin simultaneous bidirectional parallel link with per-pin skew compensation Yeung, E.; Horowitz, M.A.; Solid-State Circuits, IEEE Journal of Volume 35, Issue 11, Nov. 2000 Page(s):1619 - 1628 Digital Object Identifier 10.1109/4.881207
	AbstractPlus References Full Text: PDF(236 KB) IEEE JNL Rights and Permissions
m	7. An energy-efficient skew compensation technique for high-speed skew-sensitive signailing Lei Wang; Circuits and Systems, 2005, ISCAS 2005, IEEE International Symposium on 23-26 May 2005 Page(s):1658 - 1661 Vol. 2 Digital Object Identifier 10.1109/ISCAS.2005.1464923
	AbstractPlus Full Text: PDF(120 KB) IEEE CNF Rights and Permissions
	8. A 2.2 Gbps CMOS look-ahead DFE receiver for multidrop channel with pin-to-pin time skew compensation Young-Soo Sohn; Seung-Jun Bae; Hong-June Park; Chang-Hyun Kim; Soo-In Cho; Custom Integrated Circuits Conference, 2003, Proceedings of the IEEE 2003 21-24 Sept. 2003 Page(s):473 - 476 Digital Object Identifier 10.1109/CICC.2003.1249443 AbstractPlus Full Text: PDF(314 KB) IEEE CNF Rights and Permissions
	9. A 330 MHz low-jitter and fast-locking direct skew compensation DLL Joo-Ho Lee; Seon-Ho Han; Hoi-Jun Yoo; Solid-State Circuits Conference, 2000. Digest of Technical Papers, ISSCC, 2000 IEEE International 7-9 Feb. 2000 Page(s):352 - 353 Digital Object Identifier 10.1109/ISSCC.2000.839812 AbstractPlus Full Text: PDF(228 KB) IEEE CNF Rights and Permissions
	10. A 5-GByte/s data-transfer scheme with bit-to-bit skew control for synchronous DRAM Sato, T.; Nishio, Y.; Sugano, T.; Nakagome, Y.; Solid-State Circuits. IEEE Journal of Volume 34, Issue 5, May 1999 Page(s):653 - 660 Digital Object Identifier 10.1109/4.760375 AbstractPlus References Full Text: PDF(340 KB) IEEE 3N1. Rights and Permissions
	11. A 1.35Gbps decision feedback equalizing receiver for the SSTL SDRAM interface with 2X oversampling phase detector for skew compensation between clock and data Young-Soo Sohn; Seung-Jun Bae; Hong-June Park; Solid-State Circuits Conference, 2002, ESSCIRC 2002, Proceedings of the 28th European 24-26 Sept. 2002 Page(s):787 - 790 AbstractPlus Full Text: PDE(856 KB) IEEE CNF Rights and Permissions
	12. A CMOS-CCD signal processor for skew compensation Miura, H.; Masuda, I.; Sato, M.; Solid-State Circuits Conference, Digest of Technical Papers, 1987 IEEE International Volume XXX, Feb 1987 Page(s):112 - 113 AbstractPlus Full Text: PDF(936 KB) IEEE CNF Rights and Permissions
	13. Clock buffer chip with multiple target automatic skew compensation Watson, R.B., Jr.; Iknaian, R.B.; Solid-State Circuits, IEEE Journal of

Volume 30, Issue 11, Nov. 1995 Page(s):1267 - 1276 Digital Object Identifier 10.1109/4.475715 AbstractPlus | Full Text: PDF(1164 KB) | IEEE JNL Rights and Permissions 14. 400-MHz random column operating SDRAM techniques with self-skew compensation Hamamoto, T.; Tsukude, M.; Arimoto, K.; Konishi, Y.; Miyamoto, T.; Ozaki, H.; Yamada, M.; Solid-State Circuits, IEEE Journal of Volume 33, Issue 5, May 1998 Page(s):770 - 778 Digital Object Identifier 10.1109/4.668992 AbstractPlus | References | Full Text: PDF(300 KB) | IEEE JNL Rights and Permissions 15. Demonstration of timing skew compensation for bit-parallel WDM data transmission with picosecond precision Shen, S.; Wiener, A.M.; Photonics Technology Letters, IEEE Volume 11, Issue 5, May 1999 Page(s):566 - 568 Digital Object Identifier 10.1109/68.759400 AbstractPlus | References | Full Text: PDF(76 KB) IEEE JNL Rights and Permissions 16. A 1.0 Gb/s BiCMOS multi-channel optical Interface transmitter and receiver chip set for high resolution digital displays Gunsang Lee; Yong Sub Kim; Jae Hun Lee; Doo Hwan Choi; Suki Kim; Consumer Electronics, IEEE Transactions on Volume 47, Issue 3, Aug. 2001 Page(s):273 - 277 Digital Object Identifier 10.1109/30.964109 AbstractPlus | Full Text: PDF(518 KB) | IEEE JNL Rights and Permissions 17. An 8-Gb/s simultaneous bidirectional link with on-die waveform capture ___ Casper, B.; Martin, A.; Jaussi, J.E.; Kennedy, J.; Mooney, R.; Solid-State Circuits, IEEE Journal of Volume 38, Issue 12, Dec 2003 Page(s):2111 - 2120 Digital Object Identifier 10.1109/JSSC.2003.818569 AbstractPlus | Full Text: PDF(1471 KB) IEEE JNL Rights and Permissions 18. A phase-detect synchronous mirror delay for clock skew-compensation circuits Kuo-Hsing Cheng; Chen-Lung Wu; Yu-Lung Lo; Chia-Wei Su; Circuits and Systems, 2005, ISCAS 2005, IEEE International Symposium on 23-26 May 2005 Page(s):1070 - 1073 Vol. 2 Digital Object Identifier 10.1109/ISCAS.2005.1464777 AbstractPlus | Full Text: PDF(392 KB) | IEEE CNF Rights and Permissions 19. Low cost scheme for on-line clock skew compensation Omana, M.; Rossi, D.; Metra, C.; VLSI Test Symposium, 2005, Proceedings, 23rd IEEE 1-5 May 2005 Page(s):90 - 95 Digital Object Identifier 10.1109/VTS.2005.52 AbstractPlus | Full Text: PDF(360 KB) IEEE CNF Rights and Permissions 20. All optical bit parallel transmission systems Togneri, A.P.; Vieira Segatto, M.E.; Microwave and Optoelectronics Conference, 2003, IMOC 2003, Proceedings of the 2003 SBMO/IEEE MTT-S International Volume 1, 20-23 Sept. 2003 Page(s):367 - 372 vol.1

Rights and Permissions

AbstractPlus | Full Text: PDF(579 KB) IEEE CNF

	Yeung, E.; Horowitz, M.; Solid-State Circuits Conference, 2000, Digest of Technical Papers, ISSCC, 2000 IEEE International 7-9 Feb. 2000 Page(s):256 - 257 Digital Object Identifier 10.1109/ISSCC.2000.839774 AbstractPius Full Text: PDF(234 KB) IEEE CNF Rights and Permissions
	22. Skew detection and compensation for Internet audio applications Hodson, O.; Perkins, C.; Hardman, V.; Multimedia and Expo, 2000. ICME 2000. 2000 IEEE International Conference on Volume 3, 30 July-2 Aug. 2000 Page(s):1687 - 1690 vol.3 Digital Object Identifier 10.1109/ICME.2000.871096
	AbstractPlus Full Text: <u>PDF(</u> 356 KB) IEEE CNF Rights and Permissions
	23. Current progress of advanced high speed parallel optical links for computer clusters and switching systems Drogemuller, K.; Kuhl, D.; Blank, J.; Ehlert, M.; Kracker, T.; Hohn, J.; Klix, D.; Plickert, V.; Melchior, L.; Schmale, I.; Hildebrandt, P.; Heinemann, M.; Schiefelbein, F.P.; Leininger, L.; Wolf, HD.; Wipiejewski, T.; Ebberg, A.; Electronic Components and Technology Conference, 2000, 2000 Proceedings, 50th 21-24 May 2000 Page(s):1227 - 1235 Digital Object Identifier 10.1109/ECTC.2000.853331 AbstractPlus Full Text: PDF(824 KB) IEEE CNF Rights and Permissions
	24. Clock-buffer-chip with multiple-target automatic skew compensation Watson, R.B., Jr.; Iknaian, R.B.; Solid-State Circuits Conference, 1995. Digest of Technical Papers, 42nd ISSCC, 1995 IEEE International 15-17 Feb. 1995 Page(s):106 - 107, 345 Digital Object Identifier 10.1109/ISSCC.1995.535450 AbstractPlus Full Text: PDF(800 KB) IEEE CNF Rights and Permissions
	25. Circuit technique for skew-free clock distribution Sutoh, H.; Yamakoshi, K.; Ino, M.; Custom Integrated Circuits Conference, 1995., Proceedings of the IEEE 1995 1-4 May 1995 Page(s):163 - 166 Digital Object Identifier 10.1109/CICC.1995.518159 AbstractPlus Full Text: PDF(328 KB) IEEE CNF Bights and Permissions

1-25 | 26-41

Help Contact Us Privacy & Security IEEE.org © Copyright 2006 IEEE - All Rights Reserved

#Inspec*



Welcome United States Patent and Trademark Office

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

e-mail aprinter friendly

Results	for "(skew	comp	ensatio	n <in>m</in>	etadata)

Your search matched 41 of 1335860 documents.

A maximum of 41 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search O	ptions	1800	lify Search	•	
<u>View Session History</u>			w compensation <in>metadata)</in>	Search 3	
New Search					
			Check to search only within this results set	•	
в Кеу		Dis	alay Format: Citation C Citation & Abstract		
IEEE JNL	IEEE Journal or Magazine	√(∧ii	w selected items Select All Deselect All	. <u>1-25</u> 26-41	
IEE JNL	IEE Journal or Magazine	,			
IEEE CNF	IEEE Conference Proceeding		26. Key components of the fast reduced instruction set compu advanced bipolar differential logic and wafer scale multichl		
IEE CNF	IEE Conference Proceeding		Greub, H.J.; McDonald, J.F.; Creedon, T.; Bipolar Circuits and Technology Meeting, 1988, Proceedings of the Action of the Actio	f the 1988	
IEEE STD	IEEE Standard	ė	12-13 Sept. 1988 Page(s):19 - 22 Digital Object Identifier 10.1109/BIPOL.1988.51035		
			AbstractPlus Full Text: PDF(328 KB) IEEE CNF Rights and Permissions		
	•		27. A Source-Synchronous Double-Data-Rate Parallel Optical Teing Gui; Kiamilev, F.E.; Xiaoqing Wang; MacFadden, M.J.; Xir M.W.; Kuznia, C.; Very Large Scale Integration (VLSI) Systems, IEEE Transaction Volume 13, Issue 7, July 2005 Page(s):833 - 842 Digital Object Identifier 10.1109/TVLSI.2005.850101	ngle Wang; Waite, N.; Haney,	
			AbstractPlus Full Text: PDF(1968 KB) IEEE JNL Rights and Permissions	•	
		n	28. A reliable low-power fast skew-compensation circuit Yi-Ming Wang; Jinn-Shyan Wang; Design Automation Conference, 2004, Proceedings of the ASP Pacific 27-30 Jan. 2004 Page(s):547 - 548	-DAC 2004. Asia and South	
			AbstractPlus Full Text: PDF(313 KB) JEEE CNF Rights and Permissions	٠ .	
	·.·		29. A 1.0 Gb/s BiCMOS multi-channel optical interface transmining high resolution digital displays Gun Sang Lee; Yong Sub Kim; Jae Hun Lee; Doo Hwan CHoi; Consumer Electronics, 2001, ICCE, International Conference of 19-21 June 2001 Page(s): 2 - 3 Digital Object Identifier 10.1109/ICCE.2001.935187 AbstractPlus Full Text: PDE(220 KB) IEEE CNF Rights and Permissions	Suki Kim;	
			30. RHINET-3/SW: an 80-Gbit/s high-speed network switch for Nishimura, S.; Kudoh, T.; Nishi, H.; Yamamoto, J.; Ueno, R.; H Shikichi, Y.; Akutsu, S.; Tasho, K.; Amano, H.; Hot Interconnects.9, 2001. 22-24 Aug. 2001 Page(s):119 - 123 Digital Object Identifier 10.1109/HIS.2001.946703 AbstractPlus Full Text: PDE(536 KB) REEE CNF		

Rights and Permissions

	31. 400MHz Random Column Operating Sdram Techniques With Self Skew Compensation Hamamoto, T.; Tsukude, M.; Arimoto, K.; VLSI Circuits, 1997. Digest of Technical Papers., 1997 Symposium on June 12-14, 1997 Page(s):105 - 106
٠	AbstractPlus Full Text: PDF(228 KB) IEEE CNF Rights and Permissions
	32. Clock generation and distribution for the first IA-64 microprocessor Tam, S.; Rusu, S.; Nagarji Desai, U.; Kim, R.; Ji Zhang; Young, I.; Solid-State Circuits, IEEE Journal of Volume 35, Issue 11, Nov. 2000 Page(s):1545 - 1552 Digital Object Identifier 10.1109/4.881198
	AbstractPlus References Full Text: PDF(140 KB) IEEE JNL Rights and Permissions
	33. Integrated-optic timing tuner for high-speed WDM signals Takiguchi, K.; Shibata, T.; Itoh, M.; Photonics Technology Letters. IEEE Volume 15, Issue 7, July 2003 Page(s):948 - 950 Digital Object Identifier 10.1109/LPT.2003.813437
	AbstractPlus References Full Text: PDF(216 KB) IEEE JNL Rights and Permissions
	34. Integrated-optic encoder/decoder for time-spreading/wavelength-hopping optical CDMA Takiguchi, K.; Itoh, M.; Selected Topics in Quantum Electronics, IEEE Journal of Volume 11, Issue 2, March-April 2005 Page(s):300 - 306 Digital Object Identifier 10.1109/JSTQE.2005.846535
	AbstractPlus References Full Text: PDF(704 KB) IEEE JNL Rights and Permissions
	35. Scalable frame-synchronisation circuit for highly parallel optical interconnections [ATM] Yamakoshi, K.; Kawano, R.; Yamanaka, N.; Electronics Letters Volume 35, Issue 24, 25 Nov. 1999 Page(s):2117 - 2118 Digital Object Identifier 10.1049/el:19991434 AbstractPlus Full Text: PDE(148 KB) IEE JNL
, c	36. Proceedings. 23rd IEEE VLSI Test Symposium VLSI Test Symposium. 2005. Proceedings. 23rd IEEE 1-5 May 2005 Digital Object Identifier 10.1109/VTS.2005.2 Full Text: PDF(192 KB) IEEE CNF Rights and Permissions
	37. Fast and low-cost clock deskew buffer Omana, M.; Rossi, D.; Metra, C.; Defect and Fault Tolerance in VLSI Systems, 2004. DFT 2004. Proceedings. 19th IEEE International Symposium on 10-13 Oct. 2004 Page(s):202 - 210 Digital Object Identifier 10.1109/DFTVS.2004.1347841
	<u>AbstractPlus</u> Full Text: <u>PDF(</u> 568 KB) #표표 CNF <u>Rights and Permissions</u>
	38. Compensation for clock skew in voice over packet networks by speech interpolation Trump, T.; Circuits and Systems, 2004, ISCAS '04, Proceedings of the 2004 International Symposium on Volume 5, 23-26 May 2004 Page(s):V-608 - V-611 Vol.5
	AbstractPlus Full Text: PDF(270 KB)

 39. Dispersion characteristics of an AWG Vieira Segatto, M.E.; Wyatt, R.; Maxwell, G.D.; Taylor, J.R.; Kashyap, R.; Microwave and Optoelectronics Conference, 2001, IMOC 2001, Proceedings of the 2001 SBMO/IEEE MTT-S International Volume 1, 6-10 Aug. 2001 Page(s):167 - 169 vol.1 Digital Object Identifier 10.1109/SBMOMO.2001.1008743
AbstractPlus Full Text: PDF(299 KB) IEEE CNF Rights and Permissions
40. Bit skew and dispersion compensation in 10 Gb/s-channel bit parallel WDM systems Segatto, M.E.V.; Timofeev, F.N.; Kashyap, R.; Wyatt, R.; Lealman, I.; Harmon, R.; Taylor, J.R.; Lasers and Electro-Optics Society 2000 Annual Meeting, LEOS 2000, 13th Annual Meeting, IEEE Volume 2, 13-16 Nov. 2000 Page(s):619 - 620 vol.2 Digital Object Identifier 10.1109/LEOS.2000.893994
AbstractPlus Full Text: PDF(100 KB) 《經歷문 CNF Rights and Permissions
41. A 2 V clock synchronizer using digital delay-locked loop Chorng-Sii Hwang; Wang-Chih Chung; Chih-Yong Wang; Hen-Wai Tsao; Shen-luan Liu; ASICs, 2000, AP-ASIC 2000, Proceedings of the Second IEEE Asia Pacific Conference on 28-30 Aug. 2000 Page(s):91 - 94 Digital Object Identifier 10.1109/APASIC.2000.896916
AbstractPlus Full Text: PDF(304 KB) IEEE CNF

1-25 | 26-41

Help Contact Us Privacy & Security IEEE.org
© Copyright 2006 IEEE - All Rights Reserved

#Inspec*



Welcome United States Patent and Trademark Office

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Search Session History

y or

Edit an existing query or compose a new query in the Search Query Display.

Select a search number (#)

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

 accommond		
\$		

Search Query Display

Tue, 4 Apr 2006, 12:41:32 PM EST

Recent Search Queries		
(wu l. <in>au)</in>	144	
(skew compensation <in>metadata)</in>	41	
(skew compensation <in>metadata)</in>	41	
(wu l. <in>au)</in>	144	
(((skew compensation <in>metadata) <and> (shared bus <and> sender<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002)</and></and></in></and></and></in>	0	
(((skew compensation <in>metadata) <and> (shared bus <and> sender<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002)</and></and></in></and></and></in>	0	
(((skew compensation <in>metadata) <and> (shared bus <and> sender<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002)</and></and></in></and></and></in>	0	
(((skew compensation <in>metadata) <and> (shared bus <and> sender<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002)</and></and></in></and></and></in>	0	
(((skew compensation <in>metadata) <and> (shared bus <and> sender <or> transmitter<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002)</and></and></in></or></and></and></in>	3	
(((skew compensation <in>metadata) <and> (shared bus <and> sender <or> transmitter<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002)</and></and></in></or></and></and></in>	3	
(((skew compensation <in>metadata) <and> (shared bus <and> sender <or> transmitter<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002)</and></and></in></or></and></and></in>	3	
(((skew compensation <in>metadata) <and> (shared bus <and> sender<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002)</and></and></in></and></and></in>	0	
	(wu I. <in>au) (skew compensation<in>metadata) (wu I.<in>au) (((skew compensation<in>metadata) <and> (shared bus <and> sender<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002) (((skew compensation<in>metadata))) <and> (shared bus <and> sender<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002) (((skew compensation<in>metadata) >and> (shared bus <and> sender<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002) (((skew compensation<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002) (((skew compensation<in>metadata)) >and> (pyr >= 1950 <and> pyr <= 2002) (((skew compensation<in>metadata) >and> (shared bus <and> sender<in>metadata))) <and> (pyr >= 1950 <and> pyr <= 2002) (((skew compensation<in>metadata) <and> (shared bus <and> sender <or> transmitter<in>metadata))) <and> (shared bus <and> sender <or> transmitter<in>metadata) (shared bus <and> sender <or> transmitter<in>metadata))) <and> (shared bus <and> sender <or> transmitter<in>metadata) (shared bus <and> sender <or> transmitter<in> metadata) (shared bus <and> sender <or> transmitter<in> metadata) (shared bus <and> sender <or> transmitter<in> metadata) (shared bus <and> sender <or> transmitter<in> metadata) (shared bus <and> sender <or> transmitter<in> sender <or> trans</or></in></or></in></or></in></or></in></or></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></in></or></and></and></in></or></and></in></or></and></and></in></or></and></and></in></and></and></in></and></in></and></in></and></and></in></and></and></in></and></in></and></and></in></and></and></in></and></and></in></and></and></in></in></in></in>	





Help Contact Us Privacy & Security IEEE.org

© Copyright 2006 IEEE -- All Rights Reserved



Welcome United States Patent and Trademark Office

BROWSE

SEARCH

No Authors found beginning with letter: wu leon

IEEE XPLORE GUIDE

SUPPORT

OP1

OPTION 1

Quick Find an Author: Enter a name to locate articles written by that author.

wu leon

25

Example: Enter Lockett S to obtain a list of authors with the last name Lockett and the first initial S.

0

OPTION 2

Browse alphabetically

Select a letter from the list.

<u>ABCDEFGHIJKLMNOPQRSTUVWXYZ</u>

#Inspec

Help Contact Us Privacy & Security IEEE.org
© Copyright 2006 IEEE - All Rights Reserved



Welcome United States Patent and Trademark Office

BROWSE

SEARCH

No Authors found beginning with letter: leon wu

IEEE XPLORE GUIDE

SUPPORT

OPTION 1

Quick Find an Author:

Enter a name to locate articles written by that author.

leon wu

22

Example: Enter Lockett S to obtain a list of authors with the last name Lockett and the first initial S.

(2)

OPTION 2

Browse alphabetically

Select a letter from the list.

A B C D E F G H I J K L M N O P Q R S T U V W X Y Z

inspec

Help Contact Us Privacy & Security IEEE.org
© Copyright 2006 IEEE - All Rights Reserved



Welcome United States Patent and Trademark Office

Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "(((skew compensation<in>metadata) <and> (shared bus <and> sender<in>meta..."

e-mail printer triendly

Your search matched 0 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

View Session History

New Search

(((skew compensation < in > metadata) < and > (shared bus < and > sender < in > metada

Check to search only within this results set

» Key

IEEE Journal or

Magazine

IEEE JNL HEE JNL

IEE Journal or Magazine

IEEE CNF

IEEE Conference

Proceeding

IEE CNF

Proceeding

IEE Conference

IEEE STD IEEE Standard

Modify Search

Search 2

Display Format:

© Citation © Citation & Abstract

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistance revising your

search.

Help Contact Us Privacy & Security IEEE.org

© Copyright 2006 IEEE - All Rights Reserved

indexed by **#Inspec**



Welcome United States Patent and Trademark Office

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "(((skew compensation <in>metadata) <and> (shared bus <and> sender</and></and></in>	<or> tr"</or>
Your search matched 3 of 1335860 documents.	

e-mail printer friendly

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

View Session History

New Search

(((skew compensation<in>metadata) <and> (shared bus <and> sender <or> transr

Search 3

Check to search only within this results set

Modify Search

Display Format:

view selected items

© Citation © Citation & Abstract

IEEE JNL

IEEE Journal or

Magazine

IEE JNL

» Кеу

IEE Journal or Magazine

IEEE CNF

IEEE Conference Proceeding

IEE CNF

IEE Conference Proceeding

IEEE STD IEEE Standard

Gunsang Lee; Yong Sub Kim; Jae Hun Lee; Doo Hwan Choi; Suki Kim; Consumer Electronics, IEEE Transactions on

Select All Deselect All

Volume 47, Issue 3, Aug. 2001 Page(s):273 - 277

Digital Object Identifier 10.1109/30.964109

high resolution digital displays

AbstractPlus | Full Text: PDF(518 KB) IEEE JNL

Rights and Permissions

2. A 1.0 Gb/s BiCMOS multi-channel optical interface transmitter and receiver chip set for high resolution digital displays

1. A 1.0 Gb/s BiCMOS multi-channel optical interface transmitter and receiver chip set for

Gun Sang Lee; Yong Sub Kim; Jae Hun Lee; Doo Hwan CHoi; Suki Kim;

Consumer Electronics, 2001, ICCE, International Conference on

19-21 June 2001 Page(s):2 - 3

Digital Object Identifier 10.1109/ICCE.2001.935187

AbstractPlus | Full Text: PDF(220 KB) IEEE CNF

Rights and Permissions

3. RHiNET-3/SW: an 80-Gbit/s high-speed network switch for distributed parallel computing

Nishimura, S.; Kudoh, T.; Nishi, H.; Yamamoto, J.; Ueno, R.; Harasawa, K.; Fukuda, S.; Shikichi,

Y.; Akutsu, S.; Tasho, K.; Amano, H.;

Hot Interconnects 9, 2001.

22-24 Aug. 2001 Page(s):119 - 123

Digital Object Identifier 10.1109/HIS.2001.946703

AbstractPlus | Full Text: PDF(536 KB) | IEEE CNF

Rights and Permissions

Contact Us Privacy & Security IEEE.org

© Copyright 2006 IEEE -- All Rights Reserved

indexed by #Inspec